Huawei Hong Kong Research Center

Huawei Hong Kong Research Center was established in 2018, focusing on chipsets, software engineering, AI, and theoretical researches. We are currently seeking high-caliber Engineers and Researchers at all levels to join our new team.

(Senior) Digital Design Engineer

You will take part in defining the micro-architecture of different major functional units of a processor/AI core and contribute to the various phases of the development work, including but not limited to feasibility studies, performance/power/area estimation and optimization, benchmarking analysis, micro-architecture definition, front end design and synthesis.

Responsibilities
● Interact with architect, software teams and ASIC product development teams to define the micro-architecture of the processor/AI core including memory hierarchy and various interconnects;
● Evaluate and perform trade off analysis of hardware implementation between performance, power and area;
● Define verification and validation strategies at different levels for the processor/ AI core and carry out the related work to ensure functional correctness.

Qualifications
● Bachelor or higher degree in Electronic/Computer Engineering with experience in ASIC design/verification;
● Experience with multi-core processors /AI core architecture / design / verification;
● Experience with designs of complex high speed RTL modules;
● Good understanding of computer system architecture, memory systems or cache coherency;
● Good understanding of general CPU/GPU instruction set;
● Good understanding high speed circuit and low power design is a plus;
● Good command of written and spoken English and Chinese (Mandarin).
● Good team player with strong communication skills.

For interested parties, please send your full resume with and salary expectation to: chung.pui.yi@huawei.com. Applicants who are not invited for interview within 6 weeks may assume their application unsuccessful.