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Important Dates

Deadline for paper submission:
January 20, 2005

Notice of review results:
March 20, 2005

Early Registration:
April 10, 2005

Final Manuscript Due:
April 10, 2005

Official Website

http://icccas05.uestc.edu.cn

Only on-line submission is accepted. Please visit the website for the details.

2005 International Conference on Communications, Circuits and Systems

Call for Paper

Conference Date: May 27 – 30, 2005
Venue: The Hong Kong University of Science and Technology, Hong Kong

The third International Conference on Communications, Circuits and Systems (ICCCAS'05), to be held in Hong Kong on May 27–30, 2005, is co-organized by the Hong Kong University of Science and Technology (HKUST) and the University of Electronic Science and Technology of China (UESTC); sponsored by the Ministry of Education of China, and co-sponsored by the Chinese University of Hong Kong, the University of Hong Kong, and the City University of Hong Kong; and technically co-sponsored by the Circuits and Systems Society and Communications Society of IEEE. The conference aims to provide a broad international forum for researchers, academicians and engineers working in the area of communications, circuits, and systems to discuss state-of-the-art technologies, progress in standards, services and their applications in telecommunication and information systems. The conference will feature plenary lectures given by worldwide renowned speakers and some special sessions focused on some hot topics. Major topics to be addressed include, but are not limited to, the following areas:

1. **Communication Theory (Track CT)** (1) Algebraic coding; (2) Trellis and iterative decoding; (3) Cryptography and sequence design; (4) Space-time communications; (5) Multiuser communications; (6) Multicarrier signal processing; (7) Quantum information and computation; (8) Shannon theory and source coding; (9) Detection and estimation; (10) Others
2. **Wireless Communication (Track WC)** (1) Transmission techniques, wireless access, coding and equalization; (2) MIMO systems and space-time diversity techniques; (3) Channel modeling; (4) Multimedia wireless networks; (5) 2.5G, 3G and 4G systems; (6) Antenna and propagation; (7) Mobile satellite and GPS; (8) Wireless sensors and digital signal processing; (9) Broadband and Ultra-wideband wireless networks; (10) Others
3. **Multimedia Communications (Track MC)** (1) Multimedia networking; (2) Multimedia system architecture, performance analysis and testing; (3) Multicasting; (4) Quality of services; (5) Multimedia signal processing; (6) Multimedia content security and protection; (7) Multimedia storage, indexing, and retrieval; (8) Multimedia standards, systems, and applications; (9) Others
4. **Optical and Broadband Communications (Track OBC)** (1) Optical fiber, fiber and optoelectronic devices; (2) Optical switching and wavelength selective devices; (3) Planar lightwave circuits and waveguide devices; (4) Optical signal monitoring and conditioning; (5) Transmission systems and subsystems; (6) Optical networks; (7) Broadband networks; (8) Quality of service and performance analysis; (9) Standards and applications; (10) Others
5. **Theory and Methods of Signal Processing (Track TMSP)** (1) Filter design and wavelets; (2) Adaptive and robust signal processing; (3) Speech/audio coding and processing; (4) Image/video coding and processing; (5) Estimation and identification; (6) Stationary signals and spectral analysis; (7) Non-stationary signals and time frequency analysis; (8) Real-time signal processing systems; (9) Blind signal processing; (10) Bio-signal processing and biomedical imaging; (11) Bioinformatics and computational biology; (12) Others
6. **Neural Networks and Computational Intelligence (Track NNCI)** (1) Neural networks: theory and methods; (2) Neural networks: applications; (3) Fuzzy systems: theory and methods; (4) Fuzzy systems: applications; (5) Evolutionary computation: theory and methods; (6) Evolutionary computation: applications; (7) Others
7. **Theory, Design and Implementation of Circuits & Systems (Track TDICS)** (1) Analog/digital/mixed circuits and systems; (2) Nonlinear circuits and systems; (3) Physical design and testing of VLSI circuits and systems; (4) Circuits and systems for communications; (5) Fractional-order circuits and systems; (6) RF IC, audio, video, image ASIC design; (7) Complex networks and large scale circuits and systems; (8) Biomedical circuits and systems; (9) 3S (Remote Sensing, GIS, GPS) systems ; (10) Others

Contact Info

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