

IEEE International Conference on Field-Programmable Technology (FPT) www.icfpt.org



*Venue: The University of Tokyo, JAPAN
December 15-17, 2003
Submission Deadline: July 15, 2003*

First Call for Papers

CONFERENCE THEME

Field-programmable technologies, including complex programmable logic devices and systems containing such components, have become an important topic of research for universities, government, and industry worldwide. Field-programmable devices combine the flexibility of software with the performance of hardware. Their regular structure facilitates rapid improvement in density, capability and speed. Field-programmable systems have a wide variety of applications, such as accelerating computations in molecular biology and medical imaging, low-power control and data processing for palm-size computers, and emulating novel electronic products before manufacture; even advanced microprocessors from Intel and ARM have benefited from field-programmable hardware emulators.

The areas of interest of this conference include the following:

1) Applications of field-programmable technology:

Biomedical and scientific computation accelerators, network processors, real-time systems, rapid prototyping, hardware emulation, digital signal processing, interactive multimedia, machine vision, computer graphics, cryptography, robotics, manufacturing systems, embedded applications, evolvable and biologically-inspired hardware.

2) Design techniques and tools for field-programmable technology:

Placement, routing, synthesis, verification, technology mapping, partitioning, parallelisation, timing optimization, design and run-time environments, languages and modeling techniques, provably-correct development, intellectual property core based design, domain-specific development, hardware/software co-design.

3) Architectures for field-programmable technology:

Field programmable gate arrays, complex programmable logic devices, field programmable interconnect,

field programmable analogue arrays, field programmable arithmetic arrays, memory architectures, interface technologies, low-power techniques, adaptive devices, reconfigurable computing systems, other emerging technologies.

4) Device technology for field-programmable logic:

Programmable memories including non-volatile, dynamic and static memory cells and arrays, interconnect devices, circuits and switches, emerging VLSI device technologies.

PAPER SUBMISSIONS

The program committee solicits papers describing original research in field-programmable technology, including, but not limited to, the areas of interest indicated above. Papers should be submitted electronically in PDF format, following the IEEE style (<http://www.computer.org/cspress/instruct.htm>). Full papers should not exceed 8 pages in length, while posters should not exceed 2 pages in length. Submission guidelines and instructions will be available from <http://www.icfpt.org>.

A limited number of grants will be available to support attendance at the conference. Questions regarding the FPT conference, including the submission procedure and grants, can be sent to: fpt@icfpt.org

DEADLINES and KEY DATES

Submission of papers: July 15, 2003.
Notification of acceptance: September 20, 2003.
Final papers & registration due: October 18, 2003.

ORGANISING COMMITTEE

Kunihiro Asada (Co-Chair), The University of Tokyo
Masahiro Fujita (Co-Chair), The University of Tokyo
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Satoshi Komatsu (Local Arrangement), The University of Tokyo