The 2008 International Conference on Field-Programmable Technology (ICFPT 08)

http://www.icfpt.org

Taipei, Taiwan
Grand Formosa Regent Hotel
December 8-10, 2008

First CALL FOR PAPERS

ICFPT is an established and fast growing conference on field-programmable technologies including complex programmable logic devices and systems containing such components. Field-programmable devices combine the flexibility of software with the performance of hardware. The development and applications of field-programmable technology have become an important topic of research and development. Field-programmable systems have a wide variety of applications, such as high-performance computing systems, embedded and low-power control instruments, mobile communications, rapid prototyping and product emulation.

Submissions are solicited on topics related to field-programmable technologies, including but not limited to:

- Applications of field-programmable technology including biomedical and scientific computation accelerators, network processors, real-time systems, rapid prototyping, hardware emulation, digital signal processing, interactive multimedia, machine vision, computer graphics, cryptography, robotics, manufacturing systems, embedded applications, evolvable and biologically-inspired hardware.

- Tools and Design techniques for field-programmable technology including placement, routing, synthesis, verification, debugging, run-time support, technology mapping, partitioning, parallelization, timing optimization, design and run-time environments, languages and modeling techniques, provably-correct development, intellectual property core based design, domain-specific development, hardware/software co-design.

- Architectures for field-programmable technology including field programmable gate arrays, complex programmable logic devices, field programmable interconnect, field programmable analogue arrays, field programmable arithmetic arrays, memory architectures, interface technologies, low-power techniques, adaptive devices, reconfigurable computing systems, high-performance reconfigurable systems, evolvable hardware and adaptive computing, and fault tolerance and avoidance.

- Device technology for field-programmable logic including programmable memories such as non-volatile, dynamic and static memory cells and arrays, interconnect devices, circuits and switches, and emerging VLSI device technologies.

Important Dates
Submission of papers:
June 9, 2008

Notification of acceptance:
August 29, 2008

Camera-ready papers and author registration due:
September 22, 2008

Special Issue
Highly ranked papers will be invited for consideration for a special issue of the ACM Transactions on Reconfigurable Technology and Systems (TRETS).

Submission Guidelines
The program committee solicits papers describing original research in field-programmable technology, including, but not limited to, the areas of interest indicated above. Papers should be submitted electronically in PDF format, following the IEEE style. Full papers should not exceed 8 pages in length, while posters should not exceed 4 pages in length. Manuscripts must not identify authors or their affiliations. Papers that identify authors will NOT be considered. Submissions must be made via the conference web.

Proposals for half and full day tutorials in the areas of interest are also sought.   Inquiries should be sent to: icfpt@larc.ee.nthu.edu.tw. For the design contest check http://www.doc.ic.ac.uk/~pjamieso/FPT_DESIGN_08/.

Organizing Committee

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